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☐ 1. `Defensive programming' in the rapid development of a parallel scientific program
Cheng, D.Y.; Deutsch, J.T.; Dutton, R.W.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on
Volume 9, Issue 6, June 1990 Page(s):665 - 669
Digital Object Identifier 10.1109/43.55196

AbstractPlus | Full Text: PDF(572 KB)   **IEEE JNL**

☐ 2. **Emerging on-ship debugging techniques for real-time embedded systems**
MacNamee, C.; Heffernan, D.;
Computing & Control Engineering Journal
Volume 11, Issue 6, Dec. 2000 Page(s):295 - 303

AbstractPlus | Full Text: PDF(508 KB)   **IEE JNL**

☐ 3. **Handling timing errors in distributed programs**
Gordon, A.J.; Finkel, R.A.;
Software Engineering, IEEE Transactions on
Volume 14, Issue 10, Oct. 1988 Page(s):1525 - 1535
Digital Object Identifier 10.1109/32.6197

AbstractPlus | Full Text: PDF(1128 KB)   **IEEE JNL**

☐ 4. **Distributed performance monitoring: methods, tools, and applications**
Hofmann, R.; Klar, R.; Mohr, B.; Quick, A.; Siegle, M.;
Parallel and Distributed Systems, IEEE Transactions on
Volume 5, Issue 6, June 1994 Page(s):585 - 598
Digital Object Identifier 10.1109/71.285605

AbstractPlus | Full Text: PDF(1348 KB)   **IEEE JNL**

☐ 5. **Application-dependent dynamic monitoring of distributed and parallel systems**
Ogle, D.M.; Schwan, K.; Snodgrass, R.;
Parallel and Distributed Systems, IEEE Transactions on
Volume 4, Issue 7, July 1993 Page(s):762 - 778
Digital Object Identifier 10.1109/71.238299

AbstractPlus | Full Text: PDF(1712 KB)   **IEEE JNL**

☐ 6. **IEEE standard for boot (initialization configuration) firmware: core requirements and practices**
IEEE Std 1275-1994

1994 Page(s):i - 262

AbstractPlus | Full Text: PDF(12864 KB)   IEEE STD

7. **A scalable debugger for massively parallel message-passing programs**
Sistare, S.; Allen, D.; Bowker, R.; Jourdenais, K.; Simons, J.; Title, R.;
Parallel & Distributed Technology: Systems & Applications, IEEE [see also IEEE Concurrency]
Volume 2, Issue 2, Summer 1994 Page(s):50 - 56
Digital Object Identifier 10.1109/88.311572

AbstractPlus | Full Text: PDF(720 KB)   IEEE JNL

8. **IEEE standard for information technology - POSIX(R) Ada language interfaces - part 1: binding for system Application Program Interface (API) - amendment 2: protocol-independent interfaces**
IEEE Std 1003.5, 1999 Edition
3 Dec. 1999

AbstractPlus | Full Text: PDF(4440 KB)   IEEE STD

9. **IEEE standard for Futurebus+, profile M (military).**
IEEE Std 896.5-1993
25 February 1994 Page(s):i

AbstractPlus | Full Text: PDF(8752 KB)   IEEE STD

10. **Observer-a concept for formal on-line validation of distributed systems**
Diaz, M.; Juanole, G.; Courtiat, J.-P.;
Software Engineering, IEEE Transactions on
Volume 20, Issue 12, Dec. 1994 Page(s):900 - 913
Digital Object Identifier 10.1109/32.368136

AbstractPlus | Full Text: PDF(1264 KB)   IEEE JNL

11. **Part 3: Carrier sense multiple access with collision detect on (CSMA/CD) access method and physical layer specifications**
IEEE Std 802.3, 2000 Edition
2000 Page(s):i - 1515

AbstractPlus | Full Text: PDF(19532 KB)   IEEE STD

12. **Processor design and implementation for real-time testing of embedded systems**
Walters, G.; King, E.; Kessinger, R.; Fryer, R.;
Digital Avionics Systems Conference, 1998. Proceedings., 17th DASC. The AIAA/IEEE/SAE
Volume 1, 31 Oct.-7 Nov. 1998 Page(s):B44/1 - B44/8 vol.1
Digital Object Identifier 10.1109/DASC.1998.741470

AbstractPlus | Full Text: PDF(668 KB)   IEEE CNF

13. **CoveT: a coverage tracker for collision events in system verification**
Raghavan, R.; Baumgartner, J.;
Performance, Computing and Communications, 1998. IPCCC '98., IEEE International
16-18 Feb. 1998 Page(s):172 - 177
Digital Object Identifier 10.1109/PCCC.1998.659944

AbstractPlus | Full Text: PDF(624 KB)   IEEE CNF

14. **A hybrid monitor for behavior and performance analysis of distributed systems**
Haban, D.; Wybranietz, D.;
Software Engineering, IEEE Transactions on
Volume 16, Issue 2, Feb. 1990 Page(s):197 - 211
Digital Object Identifier 10.1109/32.44382

AbstractPlus | Full Text: PDF(1464 KB)   IEEE JNL

15. **The role of trace modulation in building mobile computing systems**

Satyanarayanan, M.; Noble, B.;
Operating Systems, 1997., The Sixth Workshop on Hot Topics in
5-6 May 1997 Page(s):135 - 139
Digital Object Identifier 10.1109/HOTOS.1997.595196

AbstractPlus | Full Text: PDF(404 KB)   IEEE CNF

16. **IEEE standard for scalable coherent interface (SCI).**
IEEE Std 1596-1992
2 August 1993 Page(s):i

AbstractPlus | Full Text: PDF(5684 KB)   IEEE STD

17. **Performance tools**
Nichols, K.M.; Dubois, D.; Janczewski, C.; Flower, J.; Flanagan, D.; Yan, J.; Malony, A.; Reed,
D.; Saraiya, N.; Snyder, L.; Krumme, D.; Couch, A.; Hideyuki, T.;
Software, IEEE
Volume 7, Issue 3, May 1990 Page(s):21 - 30
Digital Object Identifier 10.1109/52.55223

AbstractPlus | Full Text: PDF(932 KB)   IEEE JNL

18. **Programming three parallel computers**
Kallstrom, M.; Thakkar, S.S.;
Software, IEEE
Volume 5, Issue 1, Jan. 1988 Page(s):11 - 22
Digital Object Identifier 10.1109/52.1990

AbstractPlus | Full Text: PDF(952 KB)   IEEE JNL

19. **Achieving dependability throughout the development process: a distributed software
experiment**
Kelly, J.P.J.; Murphy, S.C.;
Software Engineering, IEEE Transactions on
Volume 16, Issue 2, Feb. 1990 Page(s):153 - 165
Digital Object Identifier 10.1109/32.44379

AbstractPlus | Full Text: PDF(1288 KB)   IEEE JNL

20. **An SBus Monitor Board**
Xie, H.A.; Forward, K.E.; Adams, K.M.; Leask, D.;
Field-Programmable Gate Arrays, 1995. FPGA '95. Proceedings of the Third International ACM
Symposium on
1995 Page(s):160 - 167

AbstractPlus | Full Text: PDF(128 KB)   IEEE CNF

21. **A case study of C.mmp, Cm*, and C.vmp: Part I—Experiences with fault tolerance in
multiprocessor systems**
Siewiorek, D.P.; Kini, V.; Mashburn, H.; McConnel, S.; Tsao, M.;
Proceedings of the IEEE
Volume 66, Issue 10, Oct. 1978 Page(s):1178 - 1199

AbstractPlus | Full Text: PDF(2332 KB)   IEEE JNL

22. **DAQSIM: a data acquisition system simulation tool**
Booth, A.W.; Botlo, M.; Dorenbosch, J.; Kapoor, V.S.; Milner, E.C.; Wang, C.C.; Wang, E.M.;
Nuclear Science, IEEE Transactions on
Volume 40, Issue 4, Part 1-2, Aug 1993 Page(s):788 - 793
Digital Object Identifier 10.1109/23.256662

AbstractPlus | Full Text: PDF(548 KB)   IEEE JNL

23. **The ENTRAPID protocol development environment**
Huang, X.W.; Sharma, R.; Keshav, S.;
INFOCOM '99. Eighteenth Annual Joint Conference of the IEEE Computer and Communications

Societies. Proceedings. IEEE
Volume 3, 21-25 March 1999 Page(s):1107 - 1115 vol.3
Digital Object Identifier 10.1109/INFCOM.1999.751666

AbstractPlus | Full Text: PDF(804 KB)   IEEE CNF

24. **Application of scan hardware and software for debug and diagnostics in a workstation environment**
Dervisoglu, B.I.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on
Volume 9, Issue 6, June 1990 Page(s):612 - 620
Digital Object Identifier 10.1109/43.55191

AbstractPlus | Full Text: PDF(800 KB)   IEEE JNL

25. **Digital system simulation: methodologies and examples**
Olukotun, K.; Heinrich, M.; Ofelt, D.;
Design Automation Conference, 1998. Proceedings
15-19 Jun 1998 Page(s):658 - 663

AbstractPlus | Full Text: PDF(612 KB)   IEEE CNF

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New development tool standard with the IEEE-ISTO
R Stence, MSP Sector - PROC IEEE INT CONF COMPUT DES VLSI COMPUT PROCESS, 1999 - doi.ieeecomputersociety.org
... The **packet** information is called a Transfer Code (TCODE ... reducing the information
f?om the **debug** port to ... sending the required addresses to **trace** the instruction ...
Web Search - ieeexplore.ieee.org - ieeexplore.ieee.org - csa.com

High-Bandwidth **Trace** Collection for Multicomputer Performance Monitoring
CE Hudnall Jr, DL Ledlow - System Theory, 1995., Proceedings of the Twenty-Seventh ..., 1995 - doi.ieeecs.org
... Breakpoint-style **debug**- ging support is also provided via ... in length and contain
performance **trace** data ... The SPInet logic validates the ixlumhg **packet** stream for ...
Web Search - doi.ieeecomputersociety.org - ieeexplore.ieee.org - ieeexplore.ieee.org

The **Packet** Filter: An Efficient Mechanism for User-level Network Code
JC Mogul, RF Rashid, MJ Accetta - ACM SIGOPS Operating Systems Review, 1987 - portal.acm.org
... it much harder to write and **debug**: • Each time a ... with minor modification for use
in a multi- **processor**. ... packets flowing among hosts; a **packet trace** makes it ...
Cited by 200 - Web Search - gatekeeper.dec.com - acme.ibilce.unesp.br - digital.com - all 19 versions »

A VLIW architecture for a **trace** scheduling compiler
RP Colwell, RP Nix, JJ O'Donnell, DB Papworth, PK ... - IEEE Transactions on Computers, 1988 - doi.ieeecomputersociety.org
... A &AL VLm These were the goals for the **TRACE processor** design: I a modular design,
with an expandable number of func- tional units; I use standard, high-volume ...
Cited by 305 - Web Search - doi.ieeecs.org - portal.acm.org - crhc.uiuc.edu - all 11 versions »

Trace-based mobile network emulation
BD Noble, M Satyanarayanan, GT Nguyen, RH Katz, PA ... - ACM SIGCOMM Computer Communication Review, 1997 - portal.acm.org
... Our **trace** collection mechanism differs from the Berkeley **Packet** Filter in that we
record device char- acteristics in addition to information from packets. ...
Cited by 108 - Web Search - daedalus.cs.berkeley.edu - eecs.umich.edu - www-cgi.cs.cmu.edu - all 21 versions »

Using Scan Technology for **Debug** and Diagnostics in a Workstation Environment
BI Dervisoglu, AC Inc, MA Chelmsford - ITC, 1988 - ieeexplore.ieee.org
... a significant impact on system **debug** capabilities that ... was made for an MC68K family
processor mainly because ... dbus—address, 4—bit data—**packet**—length and ...
Cited by 6 - Web Search - ieeexplore.ieee.org

Interface-based design
JA Rowson, A Sangiovanni-Vincentelli - PROC DES AUTOM CONF, 1997 - doi.ieeecomputersociety.org
... To **debug** system functionality, we can pass these tokens around ... this level consist
of a simple **trace** of the ... now explicitly choose to put the **packet** creation in ...
Cited by 174 - Web Search - portal.acm.org - sigda.org - www-cad.eecs.berkeley.edu - all 14 versions »

[PS] Efficient Simulation of Parallel Computer Systems
RG Convington, S Dwarkadas, JR Jump, JB Sinclair, ... - International Journal in Computer Simulation, 1991 - cs.rochester.edu
... TRAPP is a **trace/debug** tool for RPPT programs. ... can be repeated to generate additional
trace information and we ... The act of relaying a **packet** requires that the ...
Cited by 54 - View as HTML - Web Search - wotug.kent.ac.uk

Silicon **Debug**: Scan Chains Alone Are Not Enough
GJ van Rootselaar, B Vermeulen - IEEE Proceedings International Test Conference, 1999 - doi.ieeecomputersociety.org
... in stream"\ data 2 "**packet** data"\ header ... Example **debug** script Note that the programming
of the ... reset (starts functional execution) ; while { **trace** is not ...
Cited by 14 - Web Search - ieeexplore.ieee.org - portal.acm.org - portal.acm.org

The Transaction-Based Verification Methodology
DS Brahme, S Cox, J Gallo, M Glasser, W Grundmann, ... - Cadence Berkeley Labs, Technical Report# CDNL-TR-2000-0825,
..., 2000 - testbuilder.net
... case, s directed random tests with constraints, s **trace**-driven tests that ... into
transactions, it becomes easier to write tests, **debug** a simulation ... **packet** router ...
Cited by 9 - View as HTML - Web Search - testbuilder-jp.com - testbuilder.net - testbuilder.net

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